



# International Journal of Engineering and Science Invention

e-ISSN: 2319 – 6734 p-ISSN: 2319 – 6726

## CERTIFICATE

It is certify that the paper entitled by “*VERILOG Implementation of ALU Based Address Generation for Memory*” has been published in International Journal of Engineering and Science Invention (IJESI).

**Your article has been published with following details:**

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**Editor-In-Chief**

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